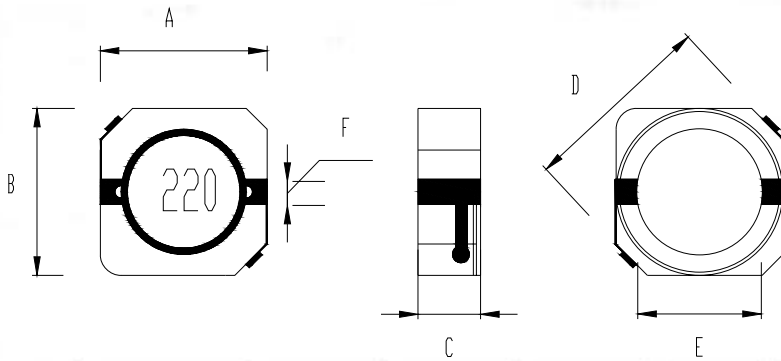


## 1. Features

1. Low profile very effective in space-conscious applications
2. Low resistance and high energy storage.

## 2. Dimension



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)
SSL10D30	10.0±0.5	10.0±0.5	3.1MAX	13.5MAX	7.7±0.3	3.0±0.1

Units: mm

## 3. Part Numbering



A:Series

B:Dimension      Ax C

C:Inductance

D:Inductance Tolerance

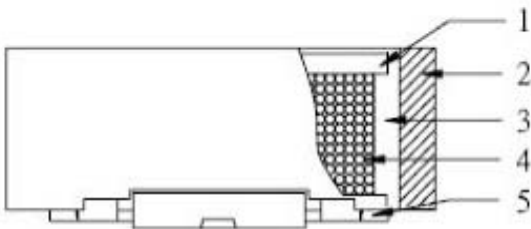
220=22uH

M=±20%, Y=±30%

## 4. Specification

Part Number	Inductance (uH)	Test Frequency (Hz)	DCR (Ω) MAX
SSL10D30-100M	10±20%	1V/100K	0.06
SSL10D30-220M	22±20%	1V/100K	0.11

## 5. Material List

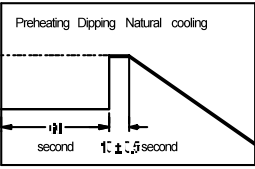
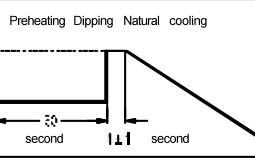


NO	ITEM	MATERIAL
1	CORE	FERRITE CORE (DR TYPE)
2	CORE	FERRITE CORE (RI TYPE)
3	GLUE	G500
4	WIRE	ENAMELLED COPPER WIRE
5	CLIP	SM212-032ET2N

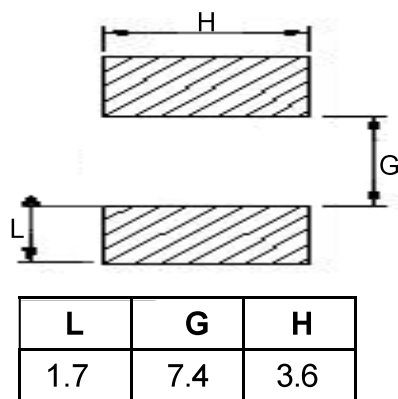
## 6. Schematic Diagram



### 7. Reliability and Test Condition

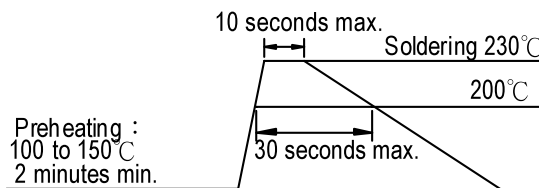
Item	Performance	Test Condition															
Operating Temperature	-20~+105°C																
Storage temperature	-40~+85°C																
Rated Current	Base on temp. rise & $\Delta L/LOA=25\%$ typ.																
Temperature Rise Test	40°C max. ( $\Delta t$ )																
Solder heat Resistance	Appearance: No significant abnormality Inductance change: Within $\pm 20\%$ .	 <p>Preheat:150°C ,60sec. Solder : H63A Solder temperature:260±5°C Flux: rosin Dip time:10±0.5sec.</p>															
Solderability	More than 90% of the terminal electrode should be covered with solder.	 <p>Preheat:125±25°C ,60sec. Solder : H63A Solder temperature:230±5°C Flux: rosin Dip time:4±1sec.</p>															
Thermal shock	Appearance: no damage. Inductance: within±20%of initial value.	<table border="1" data-bbox="742 929 1045 1164"> <thead> <tr> <th>Phase</th> <th>Temperature(°C)</th> <th>Time(min)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-25±2°C</td> <td>30±3</td> </tr> <tr> <td>2</td> <td>Room Temp.</td> <td>15</td> </tr> <tr> <td>3</td> <td>+85±2°C</td> <td>30±3</td> </tr> <tr> <td>4</td> <td>Room Temp.</td> <td>15</td> </tr> </tbody> </table> <p>For SSL Condition for 1 cycle Step1:-25±2°C 30±3 min. Step2:Room temperature 15 min. Step3:+85±5°C 30±3 min. Step4: Room temperature 15 min. Number of cycles:50 Measured:50 times</p>	Phase	Temperature(°C)	Time(min)	1	-25±2°C	30±3	2	Room Temp.	15	3	+85±2°C	30±3	4	Room Temp.	15
Phase	Temperature(°C)	Time(min)															
1	-25±2°C	30±3															
2	Room Temp.	15															
3	+85±2°C	30±3															
4	Room Temp.	15															
Humidity Resistance Test	Appearance: no damage. Inductance: within±20%of initial value.	Temperature:40±2°C. Applied current:rated current. Duration:500 hrs. Humidity:90~95%															
High Temperature Resistance Test	Appearance: no damage. Inductance: within±20%of initial value.	Temperature:85±2°C. Applied current:rated current. Duration:500 hrs.															

### 8. Recommended PC Board Pattern



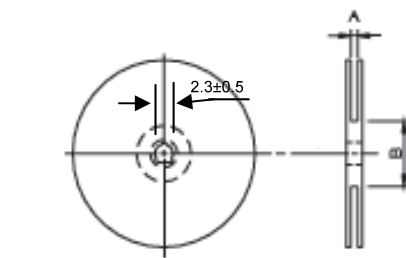
Units: mm

#### RECOMMENDED SOLDERING CONDITIONS REFLOW SOLDERINGS

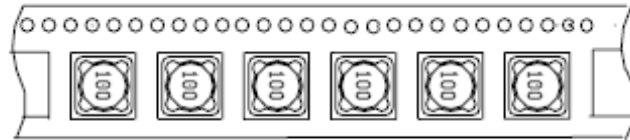
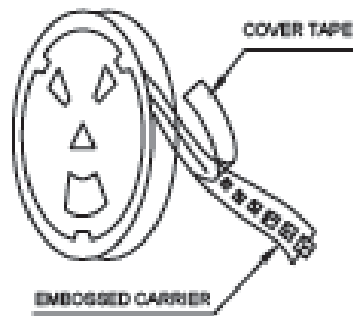


## 9. Packaging Information

### 9-1. Reel Dimension & Tape Dimension



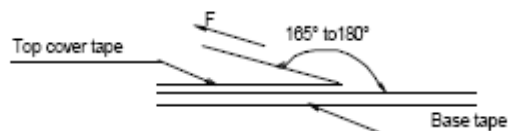
Type	A(mm)	B(mm)
16 x 24mm	24±0.5	100±0.5



### 9-2. Packaging Quantity

SSL	10D30
Chip / Reel	1000
Carton	8000
Reel Style	16x24mm

### 9-3. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~65	860~1060	300

#### Application Notice

##### • Storage Conditions

To maintain the solderability of terminal electrodes:

1. Temperature and humidity conditions: Less than 40°C and 70% RH.
2. Recommended products should be used within 6 months from the time of delivery.
3. The packaging material should be kept where no chlorine or sulfur exists in the air.

##### • Transportation

1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.